

**WATUMULL INSTITUTE OF ELECTRONICS**  
**ENGINEERING AND COMPUTER TECHNOLOGY**  
**47, DR. R. G. THADANI MARG, WORLI,**  
**MUMBAI -400 018.**

**SCHEDULE FOR ADMISSION PROCESS FOR VACANCY ROUND**  
**CANDIDATE**

- i) Last date for receiving application : 12<sup>th</sup> September, 2009 (5.00p.m)
- ii) Display of Merit List on the : 14<sup>th</sup> September,2009(11.00a.m)  
notice board at Watumull  
Institute, Worli,  
Mumbai 400 018.
- iii) Candidates to report for : 15<sup>th</sup> September,2009(11.30.a.m)  
counseling and payment  
of fees for admission.
- iv) Classes Begin : 15<sup>th</sup> September, 2009

**Venue for Admission**

**SEMINAR HALL, 6TH FLOOR, WATUMULL INSTITUTE OF  
ELECTRONICS ENGINEERING & COMPUTER TECHNOLOGY WORLI  
MUMBAI-400018**

**IMPORTANT NOTE:-**

**BRING FOLLOWING DOCUMENT ALONGWITH.**

- 1} DEMAND DRAFT IN FAVOUR OF “PRINCIPAL, WATUMULL  
INSTITUTE” FOR RS.62841/-
- 2} ALL RELATED DOCUMENT IN ORIGINAL
- 3} 2 PHOTOGRAPHS (2CM X 1.5CM)
- 4} 3 SETS OF XEROX COPIES OF ALL CERTIFICATES DULY  
ATTESTED.

**(Mrs. Sandhya Desai)**  
**Principal**

**Date:-9<sup>th</sup> September, 2009**