

WATUMULL INSTITUTE OF ELECTRONICS
ENGINEERING & COMPUTER TECHNOLOGY

WORLI, MUMBAI -400 018.

SCHEDULE OF ADMISSION PROCESS FOR ALL INSTITUTE QUOTA
CANDIDATES

- | | | |
|----|-----------------------------------------------------------------------------------------------------|----------------------------------------------------|
| a. | Last date for receiving applications | :- 13 th July, 2009 (5 P.M) |
| b. | Display of Provisional Merit List on the notice board at Watumull Institute Worli, Mumbai- 400 018. | :- 15 th July, 2009 (2 P.M.) |
| c. | Submission of Grievances Applications, if any, in writing to the College Office | :- 16 th July, 2009 (Latest by 12 Noon) |
| d. | Display of Corrected Merit List on the notice board at Watumull Institute, Worli, Mumbai-400 018. | :- 16 th July, 2009 (5 P.M.) |
| e. | Institute Candidates to report for counselling and payment of fees for admission | :- 19 th July, 2009 (3 P.M.) |
| f. | End of Admission Process | :- According to DTE directives |
| g. | Classes Begin
(ORIENTATION PROGRAMME) | :- 3 rd August, 2009 |

Venue for Admission

RAMA WATUMULL AUDITORIUM
K.C.College,
Dinshaw Wachha Road,
Churchgate, Mumbai- 400 020.

IMPORTANT NOTE:-

BRING FOLLOWING DOCUMENTS ALONGWITH

- 1] DEMAND DRAFT IN FAVOUR OF "PRINCIPAL, WATUMULL INSTITUTE" AMOUNTING Rs. 62841/-

- 2] ALL RELATED DOCUMENTS IN ORIGINAL
- 3] 2 PHOTOGRAPHS (2cm x 1.5 cm)
- 4] 3 SETS OF XEROX COPIES OF ALL CERTIFICATES DULY ATTESTED.

By Order

(Mrs. Sandhya Desai)
PRINCIPAL

Date:- 15.7.2009